

DG5000 Pro Series

Function/Arbitrary Waveform Generator _____

Performance Verification
Jun. 2025

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1 Safety Requirement

1.1 General Safety Summary

Please review the following safety precautions carefully before putting the instrument into operation so as to avoid any personal injury or damage to the instrument and any product connected to it. To prevent potential hazards, please follow the instructions specified in this manual to use the instrument properly.

Use the BNC Output Connectors Properly.

The front-panel BNC output connectors only allow signal output but do not support signal input.

Use Proper Power Cord.

Only the exclusive power cord designed for the instrument and authorized for use within the local country could be used.

• Ground the Instrument.

The instrument is grounded through the Protective Earth lead of the power cord. To avoid electric shock, it is essential to connect the earth terminal of the power cord to the Protective Earth terminal before connecting any inputs or outputs.

Observe All Terminal Ratings.

To avoid fire or shock hazard, observe all ratings and markers on the instrument and check your manual for more information about ratings before connecting the instrument.

Use Proper Overvoltage Protection.

Ensure that no overvoltage (such as that caused by a bolt of lightning) can reach the product. Otherwise, the operator might be exposed to the danger of an electric shock.

Do Not Operate Without Covers.

Do not operate the instrument with covers or panels removed.

Do Not Insert Objects Into the Air Outlet.

Do not insert anything into the holes of the fan to avoid damaging the instrument.

Use Proper Fuse.

Please use the specified fuses.

Avoid Circuit or Wire Exposure.

Do not touch exposed junctions and components when the unit is powered on.



Do Not Operate With Suspected Failures.

If you suspect damage occurs to the instrument, have it inspected by RIGOL authorized personnel before further operations. Any maintenance, adjustment or replacement especially to circuits or accessories must be performed by RIGOL authorized personnel.

Provide Adequate Ventilation.

Inadequate ventilation may cause an increase of temperature in the instrument, which would cause damage to the instrument. So please keep the instrument well ventilated and inspect the air outlet and the fan regularly.

Do Not Operate in Wet Conditions.

To avoid short circuit inside the instrument or electric shock, never operate the instrument in a humid environment.

Do Not Operate in an Explosive Atmosphere.

To avoid personal injuries or damage to the instrument, never operate the instrument in an explosive atmosphere.

Keep Instrument Surfaces Clean and Dry.

To avoid dust or moisture from affecting the performance of the instrument, keep the surfaces of the instrument clean and dry.

Prevent Electrostatic Impact.

Operate the instrument in an electrostatic discharge protective environment to avoid damage induced by static discharges. Always ground both the internal and external conductors of cables to release static before making connections.

Use the Battery Properly.

Do not expose the battery (if available) to high temperature or fire. Keep it out of the reach of children. Improper change of a battery (lithium battery) may cause an explosion. Use the RIGOL specified battery only.

Handle with Caution.

Please handle with care during transportation to avoid damage to keys, knobs, interfaces, and other parts on the panels.



WARNING

Equipment meeting Class A requirements may not offer adequate protection to broadcast services within residential environment.

Safety Notices and Symbols 1.2

Safety Notices in this Manual:



WARNING

Indicates a potentially hazardous situation or practice which, if not avoided, will result in serious injury or death.



CAUTION

Indicates a potentially hazardous situation or practice which, if not avoided, could result in damage to the product or loss of important data.

Safety Notices on the Product:

DANGER

It calls attention to an operation, if not correctly performed, could result in injury or hazard immediately.

WARNING

It calls attention to an operation, if not correctly performed, could result in potential injury or hazard.

CAUTION

It calls attention to an operation, if not correctly performed, could result in damage to the product or other devices connected to the product.

Safety Symbols on the Product:



Hazardous Voltage









Safety Warning Protective Earth Chassis Ground **Terminal**

Test Ground

2 Document Overview

This manual is designed to guide you to properly test the performance specifications of RIGOL DG5000 Pro Series Function/Arbitrary Waveform Generator. For the operation methods mentioned in the test procedures, refer to User Guide of this product.

Publication Number

PVB18101-1110

Format Conventions in this Manual

1. Key

The front panel key is denoted by the menu key icon. For example, indicates the "Default" key.

2. Menu

The menu item is denoted by the format of "Menu Name (Bold) + Character Shading" in the manual. For example, **Setup**.

3. Operation Procedures

The next step of the operation is denoted by ">" in the manual. For example,



Content Conventions in this Manual

DG5000 Pro Series Function/Arbitrary Waveform Generator includes the following models. Unless otherwise specified, this manual takes DG5508 Pro as an example to illustrate the performance verification methods of DG5000 Pro series.

Model	No. of Channels	Sample Rate	Max. Output Frequency
DG5252 Pro	2	2.5 GSa/s	250 MHz
DG5254 Pro	4	2.5 GSa/s	250 MHz
DG5258 Pro	8	2.5 GSa/s	250 MHz
DG5352 Pro	2	2.5 GSa/s	350 MHz
DG5354 Pro	4	2.5 GSa/s	350 MHz
DG5358 Pro	8	2.5 GSa/s	350 MHz
DG5502 Pro	2	2.5 GSa/s	500 MHz



Model	No. of Channels	Sample Rate	Max. Output Frequency
DG5504 Pro	4	2.5 GSa/s	500 MHz
DG5508 Pro	8	2.5 GSa/s	500 MHz

3 Overview

3.1 Test Preparations

Before the test, please make sure that the instrument is within the calibration period (1 year recommended) and has been operating for at least 30 minutes at a specified operating temperature $(23^{\circ}\text{C} \pm 5^{\circ}\text{C})$.

3.2 Recommended Test Equipment

Use the test equipment listed in the following table to verify the performance of DG5000 Pro. If the exact instrument model is not available, it is recommended to select the substitute equipment that can meet the following "Specification".

Table 3.1 Recommended Test Equipment

Equipment	Specification	Recommended Model
Frequency Counter	>10 MHz	Agilent 53131A
Frequency Counter	Accuracy: 0.1 ppm	Agliefit 33131A
Digital Multimeter	6½ digits	RIGOL DM3068
	-30 dBm to +20 dBm	
Power Meter	Accuracy: ±0.02 dB	Agilent E4416A
	Resolution: 0.01 dB	
Spectrum Analyzer	Min. resolution bandwidth ≤10 Hz	RIGOL RSA5032
	Bandwidth: ≥2 GHz	
Oscilloscope	Rise/Fall time measurement function	RIGOL DS70000
	Overshoot measurement function	
Cable	BNC (m)-BNC (m)	-
50 Ω Load	50 Ω/1 W	-
Power Sensor	-35 dBm to +20 dBm	Agilent N8482A
Power Sensor Cable	Used to connect the power meter and power sensor	-

Equipment	Specification	Recommended Model
Adaptor	BNC (f)-Dual Banana Plug (m)	-
Adaptor	N (f)-BNC (m)	-
Adaptor	BNC (f)-N (m)	-

3.3 Test Considerations

For better performance, all procedures should comply with the following recommendations:

- 1. Before making the performance verification test, please make sure that the instrument has been operating for at least 30 minutes at a specified operating temperature (23°C±5°C).
- 2. Each test should be performed at the specified operating temperature (23°C±5°C).
- **3.** Please reset the instrument to the factory setting before or after executing any of the tests.

3.4 Test Result Record

Record and keep the test result of each test. In the final chapter of this manual, a test result record form is provided. The form lists all the test items and their corresponding performance limits as well as spaces for users to record the test results.



TIP

It is recommended that users photocopy the test record form before each test and record the test results in the copy so that the form can be used repeatedly.

3.5 Specifications

The specification of each test item is provided in this manual. For other technical parameters, refer to DG5000 Data Sheet (download it from RIGOL website: www.rigol.com).



TIP

All the specifications are valid only when the instrument has been operating for more than 30 minutes within the specified operating temperature range (23°C±5°C).

4 Performance Verification Test

This chapter takes the CH1 of DG5508 Pro as an example to illustrate the performance verification test methods of the DG5000 Pro series. The testing methods are also applicable to other channels.

4.1 Frequency Accuracy Test

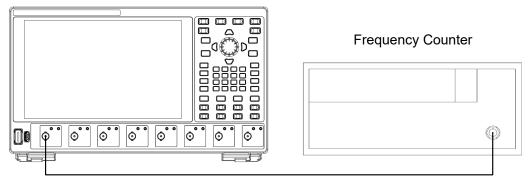
4.1.1 Specification

Frequency Accuracy			
	Typical (1 MHz frequency)		
Specification	±10 ⁻⁶ of the setting value (except Arb), 0°C to 50°C		
	$\pm 10^{-6}$ of the setting value $\pm 1~\mu$ Hz (Arb), 0°C to 50°C		

4.1.2 Test Procedures

1. As shown in the figure below, use the dual BNC cable to connect the output terminal of DG5000 Pro to the signal input terminal of the frequency counter. This case takes CH1 as an example. The test method also applies to other channels.





- **2.** Set the input impedance of the frequency counter to 1 M Ω .
- **3.** Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 4. Configure DG5000 Pro:
 - a. Set CH1 to output a Sine wave with 1 MHz frequency and 1 Vpp amplitude.
 - **b.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.

- **5.** Record the current reading on the frequency counter and verify that the reading is between 0.999999 MHz and 1.000001 MHz.
- **6.** Set DG5000 Pro to output Square, Ramp, Pulse, and Harmonics respectively (1 MHz frequency, 1 Vpp amplitude). Record the reading on the counter respectively and verify that the reading is within 0.999999 MHz and 1.000001 MHz.
- **7.** Repeat the steps above to test the frequency accuracy for other channels and record the test results.

4.1.3 Test Record Form

Channel: CH1

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH2

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency:		0.999999 MHz to 1.000001 MHz		
Square	1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH5

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	5		0.999999 MHz to 1.000001 MHz		
Square	Frequency: 1 MHz		0.999999 MHz to 1.000001 MHz		
Ramp	Amplitude:		0.999999 MHz to 1.000001 MHz		
Pulse	Τνρρ		0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency: 1 MHz Amplitude : 1 Vpp		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH8

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Note^[1]: The limit is calculated from the frequency setting value (1 MHz) \pm permissible error. The permissible error is the setting value of $\pm 10^{-6}$ (except Arb), 0°C to 50°C.

4.2 AC Amplitude Accuracy Test

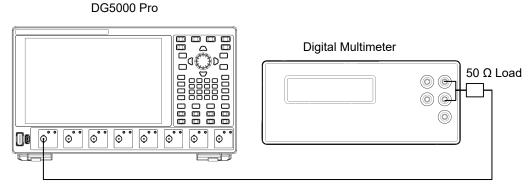
4.2.1 Specification

AC Amplitude Accuracy ^[1]					
Specification	\pm (1% of the setting value + +1 mVpp)(into 50 Ω)				

Note^[1]: 1 kHz Sine, amplitude > 1 mVpp, 0 V offset, unit: Vpp.

4.2.2 Test Procedures

1. Connect the 50 Ω BNC pass-through load and the BNC-dual banana adaptor to the voltage input terminal of the digital multimeter, and use the BNC cable to connect the 50 Ω load to the output terminal of DG5000 Pro, as shown in the figure below. This case takes CH1 as example. The test method also applies to other channels.



- **2.** Select the AC voltage (ACV) measurement function for the multimeter and set the range to "2 V".
- **3.** Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 4. Configure DG5000 Pro:
 - **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
 - **b.** Set CH1 to output a Sine wave with 1 kHz frequency, 0 Vdc offset, and 20 mVpp amplitude.
 - **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.
- **5.** Record the reading on the multimeter and verify that the reading is within the range specified in the "Limits of Amplitude Output Value (Vrms)".
- **6.** Keep other settings unchanged for DG5000 Pro and adjust the output waveform amplitude of CH1 to 100 mVpp, 500 mVpp, 1 Vpp, 5 Vpp, and 10 Vpp respectively. When the amplitude of the output waveform is set to 100 mVpp or 500 mVpp, keep the 2 V range for the multimeter; when the amplitude of the output waveform is set to 1 Vpp, 5 Vpp, or 10 Vpp, set the range to 20 V. Record the reading on the multimeter and verify that the reading is within the range specified in the "Limits of Amplitude Output Value (Vrms)".

Amplitude Setting (Vpp)	Permissible Error ^[1] (Vpp)	Limits of Amplitude Output Value (Vpp)	Limits of Amplitude Output Value (Vrms) ^[2]
20 mVpp	±1.2 mV	18.8 mVpp to 21.2 mVpp	6.6 mVrms to 7.5 mVrms
100 mVpp	±2 mV	98 mVpp to 102 mVpp	34.6 mVrms to 36.1 mVrms
500 mVpp	±6 mV	494 mVpp to 506 mVpp	174.7 mVrms to 178.9 mVrms
1 Vpp	±11 mV	0.989 Vpp to 1.011 Vpp	349.7 mVrms to 357.4 mVrms
5 Vpp	±51 mV	4.949 Vpp to 5.051 Vpp	1.7497Vrms to 1.7857 Vrms
10 Vpp	±101 mV	9.899 Vpp to 10.101 Vpp	3.4998 Vrms to 3.5712 Vrms

Note^[1]: "Permissible Error" is calculated from the specification " \pm (1% of the setting value + 1 mVpp)".

Note^[2]: "Limits of Amplitude Output Value (Vrms)" is calculated from "Limits of Amplitude Output Value (Vpp)". The conversion relationship between Vrms and Vpp: $Vpp = 2\sqrt{2} \ Vrms$.

7. Repeat the steps above to test the AC amplitude accuracy for other channels and record the test results.

4.2.3 Test Record Form

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp	Frequency: 1		34.6 mVrms to 36.1 mVrms		
500 mVpp	kHz' Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Channel: CH3

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp	Frequency: 1 kHz Offset: 0 Vdc Impedance: 50 Ω		34.6 mVrms to 36.1 mVrms		
500 mVpp			174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp	Frequency: 1 kHz		6.6 mVrms to 7.5 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	1 100101		'Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp	Frequency: 1 kHz		34.6 mVrms to 36.1 mVrms		
500 mVpp	Offset: 0 Vdc		174.7 mVrms to 178.9 mVrms		
1 Vpp	50 Ω		349.7 mVrms to 357.4 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Channel: CH8

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail		
20 mVpp			6.6 mVrms to 7.5 mVrms				
100 mVpp	Frequency: 1 kHz Offset: 0 Vdc Impedance: 50 Ω				34.6 mVrms to 36.1 mVrms		
500 mVpp			174.7 mVrms to 178.9 mVrms				
1 Vpp			349.7 mVrms to 357.4 mVrms				
5 Vpp			1.7497Vrms to 1.7857 Vrms				
10 Vpp			3.4998 mVrms to 3.5712 mVrms				

Note^[1]: 1 kHz Sine, amplitude > 1 mVpp, 0 V offset, unit: Vpp. \pm (1% of the setting value + 1 mVpp).

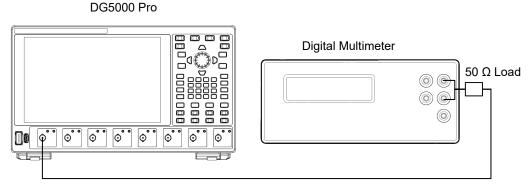
4.3 DC Offset Accuracy Test

4.3.1 Specification

DC Offset Accuracy					
Specification	\pm (1% of setting value +1 mV + 0.5% of amplitude (Vpp))(into 50 Ω)				

4.3.2 Test Procedures

1. Connect the 50 Ω BNC pass-through load and the BNC-dual banana adaptor to the voltage input terminal of the digital multimeter, and use the BNC cable to connect the 50 Ω load to the output terminal of DG5000 Pro, as shown in the figure below. This case takes CH1 as example. The test method also applies to other channels.



- **2.** Select the DC voltage (DCV) measurement function for the multimeter and set the range to 20 V.
- **3.** Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 4. Configure DG5000 Pro:
 - **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
 - **b.** Set CH1 to output a Sine wave with 1 kHz frequency, 5 Vpp amplitude, and 0 Vdc offset.
 - **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.
- **5.** Record the reading on the multimeter and verify that the reading is within the range specified in the "Offset Limit" of the table below.

6. Keep the other settings unchanged and adjust the output waveform offset of CH1 to -2.5 Vdc, -1 Vdc, -500 mVdc, 500 mVdc, 1 Vdc, and 2.5 Vdc respectively. Record the reading on the multimeter and verify that the reading is within the "Offset Limit" specified in the table below.

Offset Setting	Amplitude Setting	Permissible Error ^[1]	Offset Limit
-2.5 Vdc		±0.051 Vdc	-2.551 Vdc to -2.449 Vdc
-1 Vdc		±0.036 Vdc	-1.036 Vdc to -0.964 Vdc
-500 mVdc		±0.031 Vdc	-0.531 Vdc to -0.469 Vdc
0 Vdc	5 Vpp	±0.026 Vdc	-0.026 Vdc to +0.026 Vdc
+500 mVdc		±0.031 Vdc	0.469 Vdc to 0.531 Vdc
+1 Vdc		±0.036 Vdc	0.964 Vdc to 1.036 Vdc
+2.5 Vdc		±0.051 Vdc	2.449 Vdc to 2.551 Vdc

Note^[1]: "Permissible Error" is calculated from the specification " \pm (1% of |the setting value| + 1 mV + 0.5% of the amplitude (Vpp))".

7. Repeat the steps above to verify the DC offset accuracy for other channels and record the test result.

4.3.3 Test Record Form

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc		
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc		
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc		
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc		
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc		
2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Channel: CH3

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-2.5 Vdc	Frequency: 1		-2.551 Vdc to -2.449 Vdc		
-1 Vdc	kHz		-1.036 Vdc to -0.964 Vdc		
-500 mVdc	Amplitude: 5 Vpp		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Impedance:		-0.026 Vdc to +0.026 Vdc		
500 mVdc	50 Ω		0.469 Vdc to 0.531 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
1 Vdc			0.964 Vdc to 1.036 Vdc		
2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc		
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc		
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc		
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc		
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc		
2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Channel: CH6

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-2.5 Vdc	Frequency: 1 kHz		-2.551 Vdc to -2.449 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-1 Vdc			-1.036 Vdc to -0.964 Vdc		
-500 mVdc	A		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc		
500 mVdc	Impedance: 50 Ω		0.469 Vdc to 0.531 Vdc		
1 Vdc	30 12		0.964 Vdc to 1.036 Vdc		
2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Note^[1]: The offset limit value is calculated from the offset setting value \pm permissible error. The permissible error is calculated from the specification " \pm (1% of |the setting value| + 1 mV + 0.5% of the amplitude (Vpp))".

4.4 AC Flatness Test

4.4.1 Specification

AC Flatness	
	Typical ^[1]
Flatness	<5 MHz: ±0.1 dB
	≥5 MHz to <50 MHz: ±0.2 dB

AC Flatness	
	≥50 MHz to <100 MHz: ±0.5 dB
	≥100 MHz to <200 MHz: ±1.0 dB
	≥200 MHz: ±2.0 dB

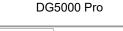
[1]: 1 μ Hz to 200 kHz relative to 1 kHz Sine, >200 kHz relative to 1 MHz Sine; 0 dBm amplitude.

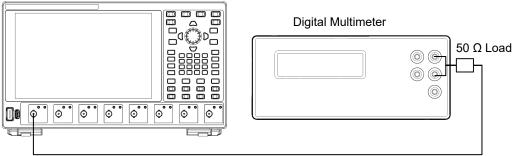
4.4.2 Test Procedures

Test Procedures

AC flatness testing is divided into low-frequency band testing (≤200 kHz) and high-frequency band testing (>200 kHz). Use a digital multimeter for low-frequency testing and a power meter for high-frequency testing.

1. Connect the 50 Ω BNC pass-through load and the BNC-dual banana adaptor to the voltage input terminal of the digital multimeter, and use the BNC cable to connect the 50 Ω load to the output terminal of DG5000 Pro, as shown in the figure below. This case takes CH1 as example. The test method also applies to other channels.





- 2. Press the front-panel Default key of DG5000 Pro and a prompt message is displayed. Click or tap OK to restore the instrument to its factory default settings.
- 3. Configure DG5000 Pro:
 - **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
 - **b.** Set CH1 to output a Sine wave with 1 kHz frequency and 0 dBm amplitude.
 - **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.

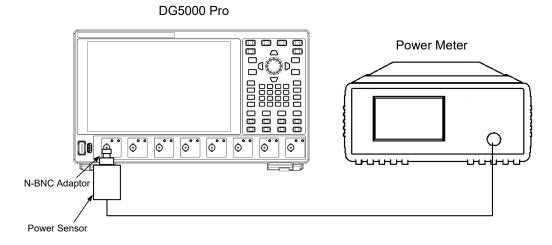
4. Select the AC voltage measurement function for the multimeter and enable the dBm operation. Set the reference resistance to 50 Ω and read the measured value as the low-frequency reference power value (P_{ref1}).



TIP

In this step, if the dBm operation is not enabled, you can also use the measured value ($V_{Reading}$) on the multimeter and the formula "dBm = 10 x lg[($V_{Reading}^2/R_{ref}$)/1 mW]" to calculate the reference power value; wherein, R_{ref} is the load impedance.

- **5.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 100 kHz. Record the measured value of the multimeter and verify that the "Measured Value-P_{ref1}" is between -0.1 dB and +0.1 dB.
- **6.** Calibrate the power meter:
 - **a.** Press the front-panel key of the power sensor to restore the power sensor to its factory defaults.
 - **b.** Connect the power sensor to the input terminal and the **[POWER REF]** terminal of the power meter respectively.
 - c. Press the Zero key to open the Zero/Cal menu. Click or tap Zero+Cal to perform the calibration. After the calibration is complete, observe whether the measured value of the power meter is 0 dBm, 50 MHz.
- 7. Disconnect DG5000 Pro and the multimeter. Use the N-BNC adaptor to connect the power sensor to the output terminal of DG5000 Pro. This case takes CH1 as an example. The test method also applies to other channels. Use the power meter cable to connect the power sensor output terminal to the power meter input terminal, as shown in the figure below.



8. Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 1 MHz. Set the center frequency of the power meter to 1 MHz. Record the measured value of the power meter as the high-frequency reference power value (P_{ref2}) .

- **9.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 2 MHz. Set the center frequency to 2 MHz for the power meter. Record the measured value and verify that the "Measured Value-P_{ref2}" is between -0.1 dB and +0.1 dB.
- 10. Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 5 MHz. Set the center frequency to 5 MHz for the power meter. Record the measured value and verify that the "Measured Value-P_{ref2}" is between -0.2 dB and +0.2 dB.
- **11.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 50 MHz. Set the center frequency to 50 MHz for the power meter. Record the measured value and verify that the "Measured Value-P_{ref2}" is between -0.5 dB and +0.5 dB.
- 12. Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 100 MHz. Set the center frequency to 100 MHz for the power meter. Record the measured value and verify that the "Measured Value- P_{ref2} " is between -1.0 dB and +1.0 dB.
- 13. Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 200 MHz. Set the center frequency to 200 MHz for the power meter. Record the measured value and verify that the "Measured Value- P_{ref2} " is between -2.0 dB and +2.0 dB.
- **14.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 500 MHz (DG5508 Pro/DG5504 Pro/DG5502 Pro only). Set the center frequency to 500 MHz for the power meter. Record the measured value and verify that the "Measured Value- P_{ref2} " is between -2.0 dB and +2.0 dB.
- **15.** Repeat the steps above to verify the AC flatness for other channels and record the test result.

4.4.3 Test Record Form

Output Channel: CH1

P_{ref1}= _____ P_{ref2}= ____

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 kHz	Amalituda, O			±0.1 dB		
2 MHz	Amplitude: 0 dBm			±0.1 dB		
5 MHz	Impedance: 50 Ω			±0.2 dB		
50 MHz	30 12			±0.5 dB		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Channe	l: CI	Н2
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$P_{ref1} =$	P _{ref2} =
· reti —	' ret2 —

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Output Channel: CH3

$P_{ref1} =$	$P_{ref2} =$

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	'Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0 dBm Impedance: 50 Ω			±0.2 dB		
50 MHz				±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Output	Channe	l:	CH4
--------	--------	----	-----

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0 dBm Impedance: 50 Ω			±0.2 dB		
50 MHz				±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Output Channel: CH5

P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0 dBm			±0.2 dB		
50 MHz	Impedance: 50 Ω			±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Output Channel: CH6

P _{ref1} = P _{ref2} =						
Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 kHz	Amplitude: 0			±0.1 dB		
2 MHz	dBm			±0.1 dB		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
5 MHz				±0.2 dB		
50 MHz				±0.5 dB		
100 MHz	Impedance: 50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

P _{ref1} =	$P_{ref2} =$
reti –	ret2 —

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0 dBm Impedance:			±0.2 dB		
50 MHz				±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Output Channel: CH8

P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass	/Fail
100 kHz				±0.1 dB		
2 MHz	Amplitude: 0 dBm Impedance: 50 Ω			±0.1 dB		
5 MHz				±0.2 dB		
50 MHz				±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	/Fail
500 MHz ^[2]				±2 dB		

Note^[1]: For 100 kHz frequency, Calculation Result = Amplitude Measured Value - P_{ref1} ; for other frequencies, Calculation Result = Amplitude Measured Value - P_{ref2} .

Note^[2]: for DG5508 Pro/DG5504 Pro/DG5502 Pro.

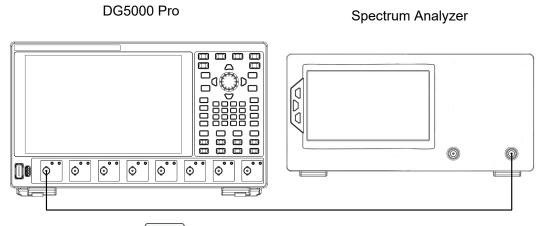
4.5 Harmonic Distortion Test

4.5.1 Specification

Sine Wave Spectral Purity					
Typical (0 dBm)					
10 Hz to <10 MHz: <-60 dBc					
≥10 MHz to <50 MHz: <-50 dBc					
≥50 MHz to <200 MHz: <-45 dBc					
≥200 MHz: <-35 dBc					

4.5.2 Test Procedures

1. As shown in the figure below, use the dual BNC cable and the BNC-BNC adaptor to connect the output terminal of DG5000 Pro to the RF input terminal of the spectrum analyzer. This case takes CH1 as an example. The test method also applies to other channels.



- 2. Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 3. Configure DG5000 Pro:

- **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
- **b.** Set CH1 to output a Sine wave with 1 MHz frequency, 0 dBm amplitude, and 0 Vdc offset.
- **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.
- **4.** Set up the spectrum analyzer:
 - a. Set the input attenuation to 20 dB and the reference level to 10 dBm.
 - **b.** Set the start frequency to 0 Hz and the stop frequency to 5 MHz.
 - c. Set the resolution bandwidth to 1 kHz.
- **5.** Enable the peak table function of the spectrum analyzer and record the measured values of the fundamental wave and the second harmonic. Verify that the harmonic distortion is less than -60 dBc through calculation^[1].
- **6.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 10 MHz.
- **7.** Set up the spectrum analyzer:
 - a. Set the input attenuation to 20 dB and the reference level to 10 dBm.
 - **b.** Set the start frequency to 5 MHz and the stop frequency to 30 MHz.
 - c. Set the resolution bandwidth to 1 kHz.
- **8.** Record the measured values of the fundamental wave and the second harmonic. Verify that the harmonic distortion is less than -50 dBc through calculation^[1].
- **9.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 50 MHz.
- **10.** Set up the spectrum analyzer:
 - a. Set the input attenuation to 20 dB and the reference level to 10 dBm.
 - **b.** Set the start frequency to 40 MHz and the stop frequency to 120 MHz.
 - c. Set the resolution bandwidth to 3 kHz.
- **11.** Record the measured values of the fundamental wave and the second harmonic in the peak table. Verify that the harmonic distortion is less than -45 dBm through calculation^[1].
- **12.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 200 MHz.

- 13. Set up the spectrum analyzer:
 - a. Set the input attenuation to 20 dB and the reference level to 10 dBm.
 - **b.** Set the start frequency to 150 MHz and the stop frequency to 500 MHz.
 - c. Set the resolution bandwidth to 3 kHz.
- **14.** Record the measured values of the fundamental wave and the second harmonic in the peak table. Verify that the harmonic distortion is less than -35 dBc through calculation^[1].
- **15.** Keep other settings unchanged for DG5000 Pro and adjust the output frequency to 400 MHz.
- **16.** Set up the spectrum analyzer:
 - a. Set the input attenuation to 20 dB and the reference level to 10 dBm.
 - **b.** Set the start frequency to 350 MHz and the stop frequency to 1 GHz.
 - c. Set the resolution bandwidth to 3 kHz.
- **17.** Record the measured values of the fundamental wave and the second harmonic in the peak table. Verify that the harmonic distortion is less than -35 dBc through calculation^[1].
- **18.** Repeat the steps above to test the harmonic distortion for other channels and record the test result.



NOTE

[1]: The Second Harmonic Distortion = Measured Value of the Second Harmonic - Measured Value of the Fundamental Wave; For example, if the measured value of the fundamental wave is -10 dBm and the measured value of the second harmonic is -72 dBm, the second harmonic distortion = (-72) - (-10) = -62 dBc.

4.5.3 Test Record Form

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/I	Fail ^[2]
1 MHz	Waveform: Sine Amplitude: 0 dBm Offset: 0 Vdc	1 (Fundamental Wave): 2:		-60 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^{[2}
10 MHz		1 (Fundamental Wave):		-50 dBc	
		2:			
50 MHz		1 (Fundamental Wave):		-45 dBc	
		2:			
200 MHz		1 (Fundamental Wave):		-35 dBc	
		2:			
400 MHz ^[3]		1 (Fundamental Wave):		-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^[2]
1 MHz	- Waveform:	1 (Fundamental Wave): 2:		<-60 dBc	
10 MHz	Sine Amplitude: 0 dBm	1 (Fundamental Wave):		<-50 dBc	
50 MHz	Offset: 0 Vdc	2: 1 (Fundamental Wave): 2:		<-45 dBc	

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail ^[2]
200 MHz		1 (Fundamental Wave): 2:		<-35 dBc		
400 MHz ^[3]		1 (Fundamental Wave): 2:		<-35 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^[2]
1 MHz		1 (Fundamental Wave): 2:		-60 dBc	
10 MHz	Waveform: Sine Amplitude: 0 dBm	1 (Fundamental Wave):		-50 dBc	
50 MHz		1 (Fundamental Wave):		-45 dBc	
200 MHz	Offset: 0 Vdc	2: 1 (Fundamental Wave): 2:		-35 dBc	
400 MHz ^[3]		1 (Fundamental Wave): 2:		-35 dBc	

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^[2]
1 MHz		1 (Fundamental Wave):		-60 dBc	
		2:			
10 MHz		1 (Fundamental Wave):		-50 dBc	
	- Waveform:	2:			
	Sine Amplitude: 0 dBm	1			
50 MHz		(Fundamental Wave):		-45 dBc	
	Offset: 0	2:			
200 MHz	- Vdc	1 (Fundamental Wave):		-35 dBc	
		2:			
400 MHz ^[3]		1 (Fundamental Wave):		-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail ^[2]
1 MHz	Waveform: Sine Amplitude: 0 dBm	1 (Fundamental Wave): 2:		-60 dBc		
10 MHz	Offset: 0 Vdc	1 (Fundamental Wave):		-50 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail ^[2]
		2:				
50 MHz		1 (Fundamental Wave): 2:		-45 dBc		
200 MHz		1 (Fundamental Wave):		-35 dBc		
400 MHz ^[3]		1 (Fundamental Wave): 2:		-35 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail ^[2]
1 MHz		1 (Fundamental Wave):		-60 dBc		
		2:				
10 MHz	Waveform: Sine Amplitude: 0 dBm Offset: 0 Vdc	1 (Fundamental Wave):		-50 dBc		
		2:				
50 MHz		1 (Fundamental Wave):		-45 dBc		
		2:				
200 MHz		1 (Fundamental Wave):		-35 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fa	ail ^[2]
		2:				
400 MHz ^[3]		1 (Fundamental Wave):		-35 dBc		
		2:				

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^{[2}]
1 MHz		1 (Fundamental Wave):		-60 dBc		
	_	2:				
10 MHz		1 (Fundamental Wave):		-50 dBc		
	Mayoform:	2:				
50 MHz	- Waveform: Sine Amplitude: 0 dBm Offset: 0	1 (Fundamental Wave):		-45 dBc		
		2:				
200 MHz	- Vdc	1 (Fundamental Wave):		-35 dBc		
		2:				
400 MHz ^[3]		1 (Fundamental Wave):		-35 dBc		
		2:				

Channel: CH8

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail ^[2]
1 MHz		1 (Fundamental Wave):		-60 dBc	
		2:			
10 MHz		1 (Fundamental Wave):		-50 dBc	
	Mayoform:	2:			
50 MHz	- Waveform: Sine Amplitude: 0 dBm Offset: 0	1 (Fundamental Wave):		-45 dBc	
		2:			
200 MHz	- Vdc	1 (Fundamental Wave):		-35 dBc	
		2:			
400 MHz ^[3]		1 (Fundamental Wave):		-35 dBc	
		2:			

Note^[1]: Calculation Result = Measured Value for the nth Harmonic—Measured Value for the Fundamental Wave

Note^[2]: If the calculation result is greater than the limit, the test fails.

Note^[3]: for DG5508 Pro/DG5504 Pro/DG5502 Pro.

4.6 Spurious Signal Test

4.6.1 Specification

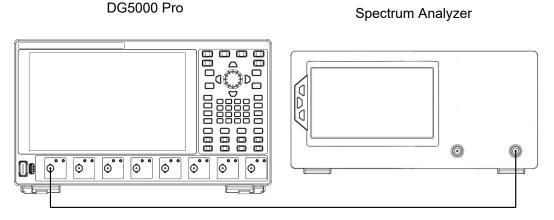
Sine Wave Spurious Signal (nonharmonic)						
	Typical (0 dBm)					
C	10 Hz to <10 MHz: <-60 dBc					
Specification	≥10 MHz to <50 MHz: <-55 dBc					
	≥50 MHz: <-45 dBc + 6 dBc/octave ^[1]					

Note^[1]: 6 dBc/octave means that for every doubling of the frequency, the specification increases by 6 dBc. For example, when DG5000 Pro outputs 50 MHz, the specification limit is -45 dBc; when it outputs 100 MHz, the limit is -45 dBc + 6 dBc.

4.6.2 Test Procedures

Test Procedures

1. As shown in the figure below, use the dual BNC cable and the BNC-BNC adaptor to connect the output terminal of DG5000 Pro to the RF input terminal of the spectrum analyzer. This case takes CH1 as an example. The test method also applies to other channels.



- 2. Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 3. Configure DG5000 Pro:
 - **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
 - **b.** Set CH1 to output a Sine wave with 1 MHz frequency, 0 dBm amplitude, and 0 Vdc offset.
 - **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.
- **4.** Set up the spectrum analyzer:
 - a. Set the reference level to 10 dBm and the input attenuation to 20 dB.

- **b.** Set the start frequency to 0 Hz and the stop frequency to 10 MHz.
- c. Set the resolution bandwidth to 1 kHz.
- d. Set the peak shift to 3 dB.
- e. Set the sweep mode to Single.
- **f.** Press the front panel Peak key to find the maximum peak.
- **5.** Measure the maximum spurious signal (except the harmonics) using the cursor measurement and record the test result A. Calculate the nonharmonic spurious signal (A 0 dBm) and compare it with the specification.
- **6.** Keep the other settings unchanged and change the output frequency of DG5000 Pro and the stop frequency of the spectrum analyzer according to the table below. Record the max. spurious signal for each output frequency and calculate the measurement result. Compare the result with the specification.

DG5000 Pro Output Frequency	Spectrum Analyzer Stop Frequency
10 MHz	100 MHz
50 MHz	150 MHz
200 MHz	500 MHz
400 MHz	1 GHz

7. Repeat the steps above to test the spurious signal for other channels and record the test result.

4.6.3 Test Record Form

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Channel: CH3

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Channel: CH4

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]	
1 MHz	0 Hz	10 MHz			-60 dBc		

Output Frequency	Start Frequency	Stop Frequency	Α	A - 0 dBm	Limit	Pass/ Fail ^[1]
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Channel: CH7

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/ Fail ^[1]
1 MHz	0 Hz	10 MHz			-60 dBc	
10 MHz	0 Hz	100 MHz			-55 dBc	
50 MHz	0 Hz	150 MHz			-45 dBc	

Output Frequency	Start Frequency	Stop Frequency	Α	A - 0 dBm	Limit	Pass/ Fail ^[1]
200 MHz	0 Hz	500 MHz			-27 dBc	
400 MHz	0 Hz	1 GHz			-3 dBc	

Note^[1]: If the calculation result is greater than the limit, the test fails.

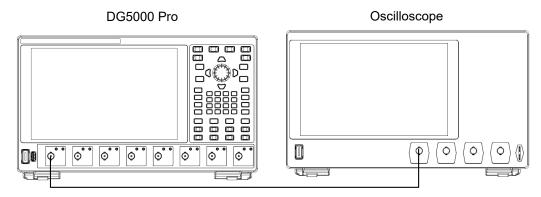
4.7 Overshoot Test

4.7.1 Specification

Square Overshoot	
Specification	Typical (fast transition disabled, 0 dBm amplitude, 10 MHz frequency), <5%

4.7.2 Test Procedures

1. As shown in the figure below, use the dual BNC cable to connect the output terminal of DG5000 Pro to the signal input terminal of the oscilloscope. This case takes CH1 as an example. The test method also applies to other channels.



- 2. Press the front-panel key of DG5000 Pro and a prompt message is displayed. Click or tap **OK** to restore the instrument to its factory default settings.
- 3. Configure DG5000 Pro:
 - **a.** Set the output impedance of CH1 to 50 Ω (in CH1 setup interface, click or tap the **Channel** tab and select "Load" in the drop-down menu of **Imped**).
 - **b.** Set the CH1 to output a Square wave with 10 MHz frequency, 0 dBm amplitude, and 0 Vdc offset.
 - **c.** Press the front-panel CH1 key to enable the output of CH1. You can also click or tap the channel identifier of the Channel Setup Interface to enable the output.

- 4. Set up the oscilloscope:
 - a. Set the vertical scale to 200 mV/div.
 - **b.** Set the horizontal timebase to 100 ns.
 - **c.** Adjust the trigger level.
 - **d.** Set the input impedance to 50 Ω .
 - e. Enable the overshoot measurement function.
- **5.** Record the measured value of the oscilloscope and verify that the value is within the specified range.
- **6.** Repeat the steps above to test the overshoot for other channels and record the test result.

4.7.3 Test Record Form

Channel: CH1

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		,F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH2

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		4F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		4F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		4E0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH5

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		4F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH6

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		4F9/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH7

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		,F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/Fail
Waveform: Square			
Frequency: 10 MHz		.50/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

5 Appendix: Test Record Form

RIGOL DG5000 Pro Series Function/Arbitrary Waveform Generator Performance Verification Test Record Form

Model:	Tested by:	Test Date:
1410acı.	16366d by	1050 Date.

Frequency Accuracy Test

Channel: CH1

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency:		0.999999 MHz to 1.000001 MHz		
Ramp	1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH2

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency:		0.999999 MHz to 1.000001 MHz		
Square	1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency:		0.999999 MHz to 1.000001 MHz		
Ramp	1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH5

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency: 1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Ramp			0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Channel: CH7

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine			0.999999 MHz to 1.000001 MHz		
Square	Frequency:		0.999999 MHz to 1.000001 MHz		
Ramp	1 MHz Amplitude: 1 Vpp		0.999999 MHz to 1.000001 MHz		
Pulse			0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Waveform	Setting	Measured Value	Limit ^[1]	Pass/	'Fail
Sine	Frequency:		0.999999 MHz to 1.000001 MHz		
Square			0.999999 MHz to 1.000001 MHz		
Ramp	1 MHz Amplitude:		0.999999 MHz to 1.000001 MHz		
Pulse	1 Vpp		0.999999 MHz to 1.000001 MHz		
Harmonic			0.999999 MHz to 1.000001 MHz		

Note^[1]: The limit is calculated from the frequency setting value (1 MHz) \pm permissible error. The permissible error is the setting value of $\pm 10^{-6}$ (except Arb), 0°C to 50°C.

AC Amplitude Accuracy Test

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp	Frequency: 1		34.6 mVrms to 36.1 mVrms		
500 mVpp	kHz Offset: 0 Vdc Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Урр			1.7497Vrms to 1.7857 Vrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	- 50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Channel: CH3

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	- 50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp	Frequency: 1		6.6 mVrms to 7.5 mVrms		
100 mVpp	kHz Offset: 0 Vdc		34.6 mVrms to 36.1 mVrms		
500 mVpp	Impedance: 50 Ω		174.7 mVrms to 178.9 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
1 Vpp			349.7 mVrms to 357.4 mVrms		
5 Vpp			1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Amplitude Setting	Setting	Measured Value	Limit ^[1]	Pass/	Fail
20 mVpp			6.6 mVrms to 7.5 mVrms		
100 mVpp			34.6 mVrms to 36.1 mVrms		
500 mVpp	Frequency: 1 kHz		174.7 mVrms to 178.9 mVrms		
1 Vpp	Offset: 0 Vdc		349.7 mVrms to 357.4 mVrms		
5 Vpp	50 Ω		1.7497Vrms to 1.7857 Vrms		
10 Vpp			3.4998 mVrms to 3.5712 mVrms		

Note^[1]: 1 kHz Sine, amplitude > 1 mVpp, 0 Vdc offset, unit: Vpp. \pm (1% of the setting value + 1 mVpp).

DC Offset Accuracy Test

Channel: CH1

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
+500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
+1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
+2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Channel: CH2

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail	
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc		
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc		
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc		
+500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc		
+1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc		
+2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-2.5 Vdc	Frequency: 1		-2.551 Vdc to -2.449 Vdc		
-1 Vdc	-1 Vdc Amplitude: 5 Vpp		-1.036 Vdc to -0.964 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1] Pass		nil
-500 mVdc			-0.531 Vdc to -0.469 Vdc		
0 Vdc			-0.026 Vdc to +0.026 Vdc		
+500 mVdc	Impedance: 50 Ω		0.469 Vdc to 0.531 Vdc		
+1 Vdc			0.964 Vdc to 1.036 Vdc		
+2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1 kHz Amplitude: 5 Vpp Impedance: 50 Ω		-1.036 Vdc to -0.964 Vdc	
-500 mVdc			-0.531 Vdc to -0.469 Vdc	
0 Vdc			-0.026 Vdc to +0.026 Vdc	
+500 mVdc			0.469 Vdc to 0.531 Vdc	
+1 Vdc			0.964 Vdc to 1.036 Vdc	
+2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
+500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
+1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
+2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1		-1.036 Vdc to -0.964 Vdc	
-500 mVdc	kHz		-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
+500 mVdc	Impedance:		0.469 Vdc to 0.531 Vdc	
+1 Vdc	50 Ω		0.964 Vdc to 1.036 Vdc	
+2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Channel: CH7

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/Fail
-2.5 Vdc			-2.551 Vdc to -2.449 Vdc	
-1 Vdc	Frequency: 1 kHz		-1.036 Vdc to -0.964 Vdc	
-500 mVdc			-0.531 Vdc to -0.469 Vdc	
0 Vdc	Amplitude: 5 Vpp		-0.026 Vdc to +0.026 Vdc	
+500 mVdc	Impedance: 50 Ω		0.469 Vdc to 0.531 Vdc	
+1 Vdc			0.964 Vdc to 1.036 Vdc	
+2.5 Vdc			2.449 Vdc to 2.551 Vdc	

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
-2.5 Vdc	Frequency: 1		-2.551 Vdc to -2.449 Vdc		
-1 Vdc	kHz		-1.036 Vdc to -0.964 Vdc		
-500 mVdc	Amplitude: 5 Vpp		-0.531 Vdc to -0.469 Vdc		
0 Vdc	Impedance:		-0.026 Vdc to +0.026 Vdc		
+500 mVdc	50 Ω		0.469 Vdc to 0.531 Vdc		

Offset Setting	Setting	Measured Value	Limit ^[1]	Pass/F	ail
+1 Vdc			0.964 Vdc to 1.036 Vdc		
+2.5 Vdc			2.449 Vdc to 2.551 Vdc		

Note^[1]: The offset limit value is calculated from the offset setting value \pm permissible error. The permissible error is calculated from the specification " \pm (1% of |the setting value| + 1 mVdc +0.5% of the amplitude (Vpp))".

AC Flatness Test

Channel: CH1

P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance: 50 Ω			±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

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P_{ref}= _____

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	/Fail
2 MHz				±0.1 dB		
5 MHz	Amaralitural ar O			±0.2 dB		
50 MHz	Amplitude: 0 dBm			±0.5 dB		
100 MHz	Impedance: 50 Ω			±1 dB		
200 MHz	30 12			±2 dB		
500 MHz ^[2]				±2 dB		

P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

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P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Channel: CH5

P_{ref1} = _____ P_{ref2} = _____

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass	/Fail
100 kHz	Amplitude: 0			±0.1 dB		
2 MHz	dBm Impedance:			±0.1 dB		
5 MHz	50 Ω			±0.2 dB		

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass	/Fail
50 MHz				±0.5 dB		
100 MHz				±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Channe	l:	CH	6
C	• •	• • •	_

P _{ref1} =	$P_{ref2} =$
ret1 —	ret2 –

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass,	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

P_{ref1}= _____ P_{ref2}= _____

Frequency Setting	Setting	Measured Value	Calculatio n Result ^[1]	Limit	Pass/	/Fail
100 kHz				±0.1 dB		
2 MHz				±0.1 dB		
5 MHz	Amplitude: 0			±0.2 dB		
50 MHz	dBm Impedance:			±0.5 dB		
100 MHz	50 Ω			±1 dB		
200 MHz				±2 dB		
500 MHz ^[2]				±2 dB		

Note^[1]: For 100 kHz frequency, Calculation Result = Amplitude Measured Value - P_{ref1} ; for other frequencies, Calculation Result = Amplitude Measured Value - P_{ref2} .

Note^[2]: for DG5508 Pro/DG5504 Pro/DG5502 Pro.

Harmonic Distortion Test

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave): 2:		<-60 dBc	
10 MHz	Waveform: Sine Amplitude: 0 dBm	1 (Fundamental Wave):		<-50 dBc	
50 MHz	Offset: 0 Vdc	1 (Fundamental Wave):		<-45 dBc	

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
200 MHz		1 (Fundamental Wave): 2:		<-35 dBc	
400 MHz ^[2]		1 (Fundamental Wave): 2:		<-35 dBc	

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave):		<-60 dBc	
		2:			
10 MHz		1 (Fundamental Wave):		<-50 dBc	
		2:			
50 MHz	Waveform: Sine Amplitude: 0 dBm	1 (Fundamental Wave):		<-45 dBc	
	Offset: 0 Vdc	2:			
200 MHz		1 (Fundamental Wave):		<-35 dBc	
		2:			
400 MHz ^[2]		1 (Fundamental Wave):		<-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave):		<-60 dBc	
		2:			
10 MHz	Waveform:	1 (Fundamental Wave):		<-50 dBc	
		2:			
50 MHz		1 (Fundamental Wave):		<-45 dBc	
	Offset: 0 Vdc	2:			
200 MHz		1 (Fundamental Wave):		<-35 dBc	
		2:			
400 MHz ^[2]		1 (Fundamental Wave):		<-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/F	ail
1 MHz	Waveform: Sine Amplitude: 0	1 (Fundamental Wave):		<-60 dBc		
10 MHz	dBm Offset: 0 Vdc	1 (Fundamental Wave):		<-50 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail
		2:				
50 MHz		1 (Fundamental Wave):		<-45 dBc		
200 MHz		1 (Fundamental Wave):		<-35 dBc		
400 MHz ^[2]		1 (Fundamental Wave): 2:		<-35 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave): 2:		<-60 dBc	
10 MHz	Waveform: Sine Amplitude: 0	1 (Fundamental Wave):		<-50 dBc	
50 MHz	dBm Offset: 0 Vdc	1 (Fundamental Wave): 2:		<-45 dBc	
200 MHz		1 (Fundamental Wave):		<-35 dBc	

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/	Fail
		2:				
400 MHz ^[2]		1 (Fundamental Wave): 2:		<-35 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave):		<-60 dBc	
10 MHz	Waveform: Sine Amplitude: 0 dBm	1 (Fundamental Wave):		<-50 dBc	
50 MHz		1 (Fundamental Wave):		<-45 dBc	
	Offset: 0 Vdc	2:			
200 MHz		1 (Fundamental Wave):		<-35 dBc	
400 MHz ^[2]		1 (Fundamental Wave):		<-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
1 MHz		1 (Fundamental Wave):		<-60 dBc	
		2:			
10 MHz		1 (Fundamental Wave):		<-50 dBc	
		2:			
50 MHz	Waveform: Sine MHz Amplitude: 0 dBm	1 (Fundamental Wave):		<-45 dBc	
	Offset: 0 Vdc	2:			
200 MHz		1 (Fundamental Wave):		<-35 dBc	
400 MHz ^[2]		(Fundamental Wave):		<-35 dBc	
		2:			

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/l	Fail
1 MHz	1 MHz Waveform: Sine Amplitude: 0	1 (Fundamental Wave):		<-60 dBc		
		2:				
10 MHz	dBm Offset: 0 Vdc	1 (Fundamental Wave):		<-50 dBc		

Frequency Setting	Setting	Measured Value	Calculation Result ^[1]	Limit	Pass/Fail
50 MHz		1 (Fundamental Wave): 2:		<-45 dBc	
200 MHz		1 (Fundamental Wave):		<-35 dBc	
		2: 1			
400 MHz ^[2]		(Fundamental Wave):		<-35 dBc	

 $Note^{[1]}$: Calculation Result = Measured Value for the n^{th} Harmonic—Measured Value for the Fundamental Wave

Note^[2]: for DG5508 Pro/DG5504 Pro/DG5502 Pro.

Spurious Signal Test

Channel: CH1

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/	'Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass	/Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/	/Fail
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/	Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Channel: CH4

Output Frequency	Start Frequency	Stop Frequency	Α	A - 0 dBm	Limit	Pass/I	Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass	/Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/	'Fail
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass	/Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Channel: CH7

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/	'Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Channel: CH8

Output Frequency	Start Frequency	Stop Frequency	A	A - 0 dBm	Limit	Pass/F	Fail
1 MHz	0 Hz	10 MHz			<-60 dBc		
10 MHz	0 Hz	100 MHz			<-55 dBc		
50 MHz	0 Hz	150 MHz			<-45 dBc		
200 MHz	0 Hz	500 MHz			<-27 dBc		
400 MHz ^[1]	0 Hz	1 GHz			<-3 dBc		

Note^[1]: for DG5508 Pro/DG5504 Pro/DG5502 Pro.

Overshoot Test

Channel: CH1

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		,F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH2

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		.50/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH3

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		.E0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH4

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		<5%	
Amplitude: 0 dBm			
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		<5%	
Amplitude: 0 dBm			
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		~E0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

Channel: CH7

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		<5%	
Amplitude: 0 dBm			
Offset: 0 Vdc			

Setting	Measured Value	Limit	Pass/ Fail
Waveform: Square			
Frequency: 10 MHz		,F0/	
Amplitude: 0 dBm		<5%	
Offset: 0 Vdc			

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